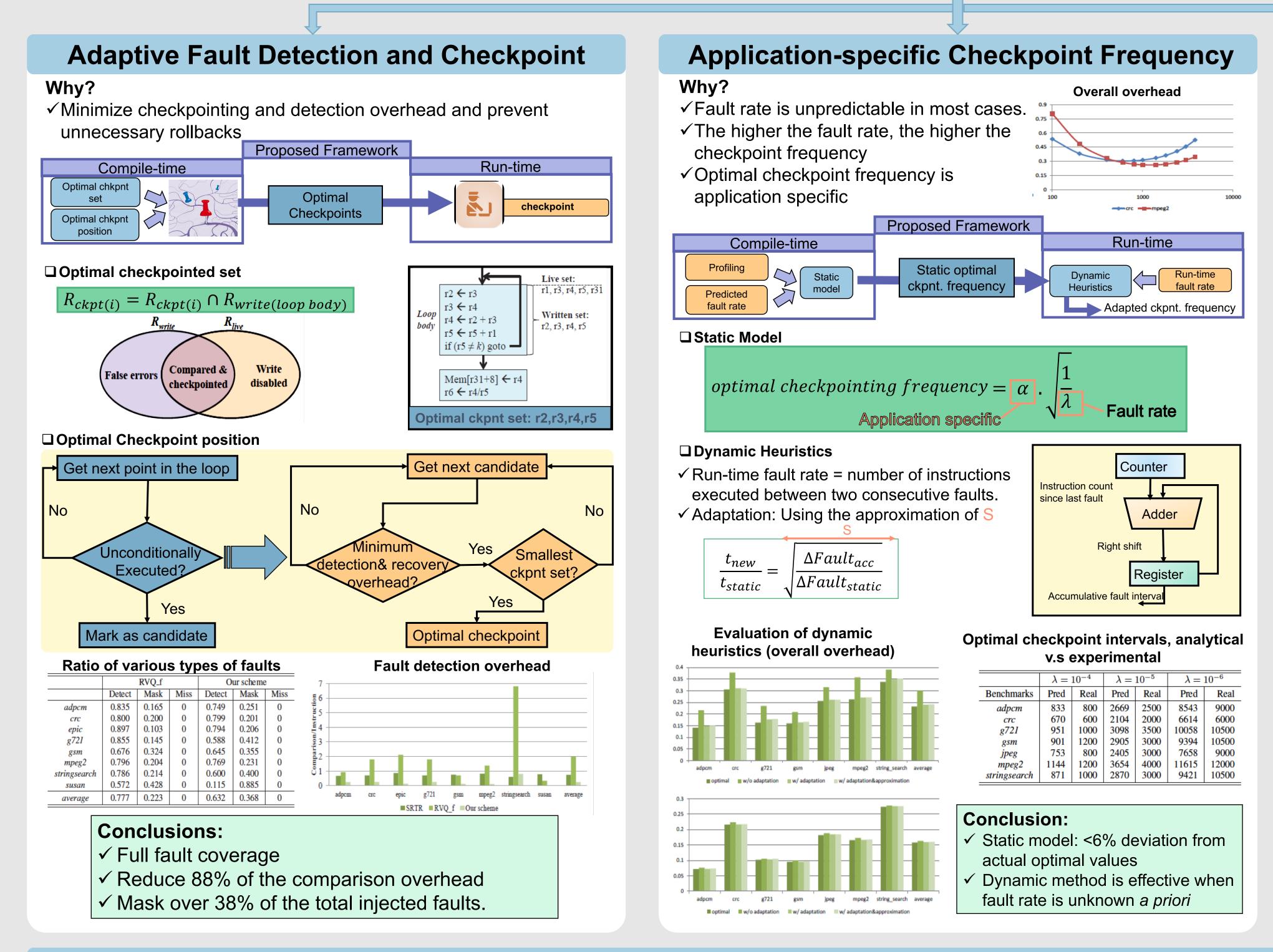


Boosting Resilience Efficiency in the Face of Frequent, Clustered, and Diverse Faults





1) F. S. Hosseini, P. Fotouhi, C. Yang, and G. R. Gao. "Leveraging compiler optimizations to reduce runtime fault recovery overhead," DAC, 2017. 2) H. A. Khouzani and C. Yang, "Towards a Scalable and Write-Free Multi-version Checkpointing Scheme in Solid State Drives," DSN, 2016. 3) C. Liu and C. Yang. "Secure and Durable (SEDURA): An Integrated Encryption and Wear-leveling Framework for PCM-based Main Memory," LCTES, 2015. 4) C. Yang and M. Ruiz Varela, "Qualifying non-volatile register files for embedded systems through compiler-directed write minimization and balancing," VLSI-SoC, 2015. 5) L. A. Rozo Duque and C. Yang, "Improving MPSoC reliability through adapting runtime task schedule based on time-correlated fault behavior," DATE, 2015. 6) L. A. Rozo Duque and C. Yang, "Guiding fault-driven adaption in multicore systems through a reliability-aware static task schedule," ASPDAC, 2015. 7) C. Liu and C. Yang, "Improving multi-level PCM reliability through age-aware reading and writing strategies," ICCD, 2014. 8) H. Chen and C. Yang. "Fault detection and recovery efficiency co-optimization through compile-time analysis and runtime adaptation," CASES, 2013. 9) H. Chen and C. Yang. "Boosting efficiency of fault detection and recovery through application-specific comparison and checkpointing," LCTES, 2013.

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Why?

Compile	-time
Minimum Recovery Set Selecter	
Ambiguity Set Minimizer	

□Minimum Recovery Set Selector (MRSS)

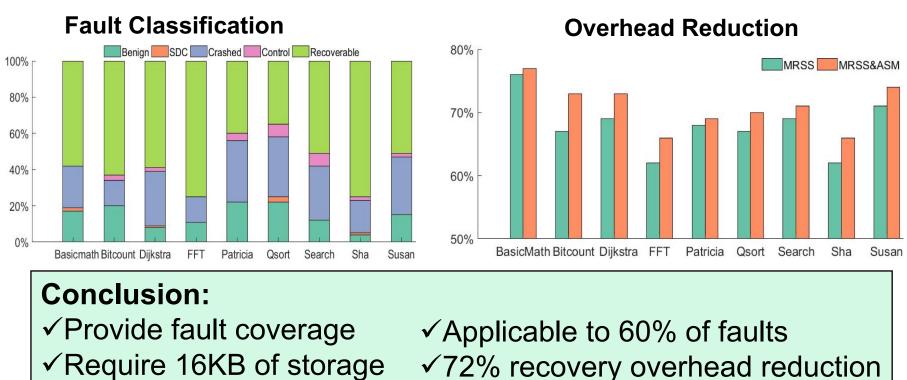
✓ Classify Instructions in each scenario:

Faulty: Valid and Fa
Clean: Valid and Cle
Ambiguous: overwri

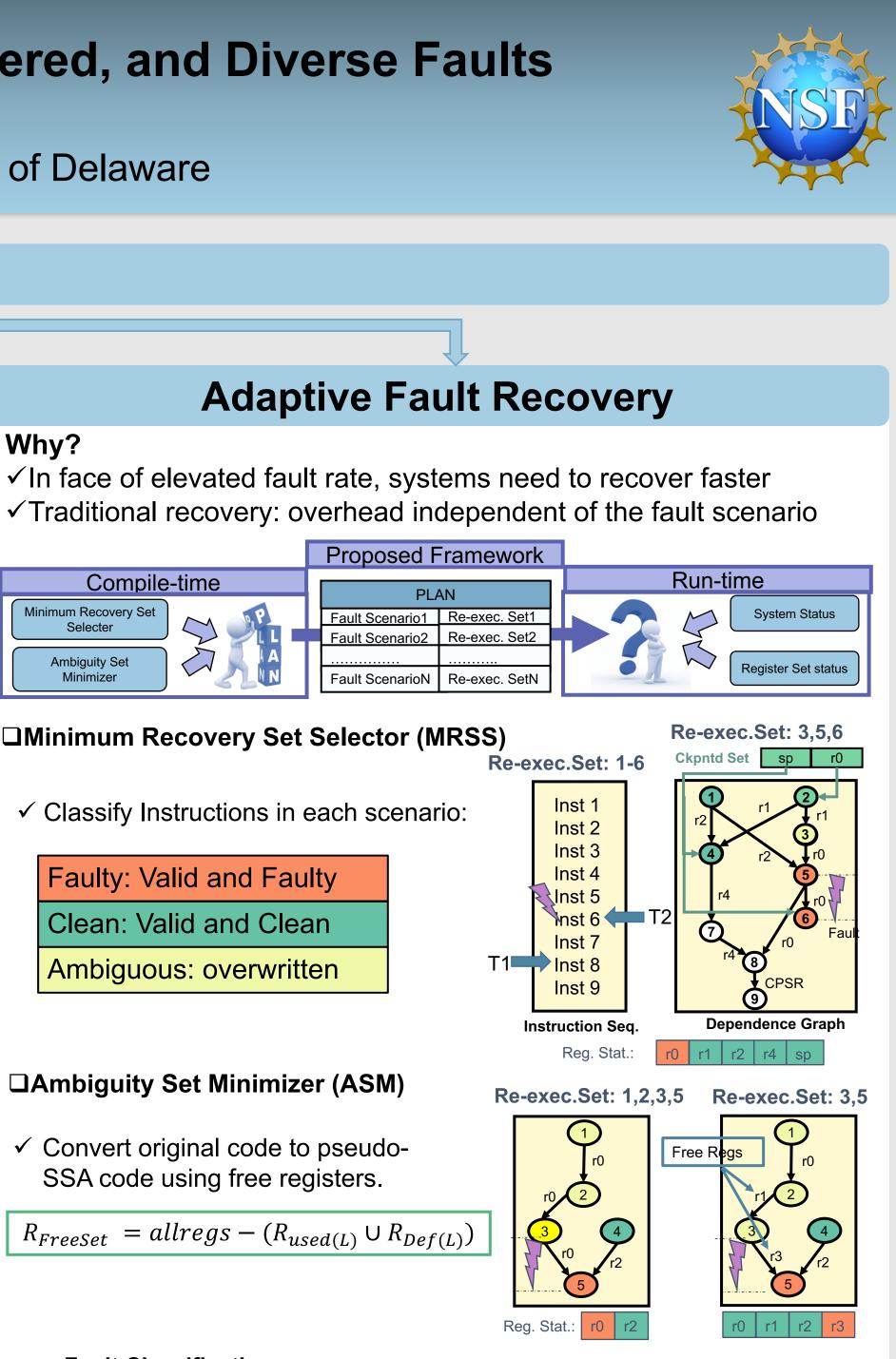
Ambiguity Set Minimizer (ASM)

✓ Convert original code to pseudo-SSA code using free registers.

 $R_{FreeSet} = allregs - (R_{used(L)} \cup R_{Def(L)})$



Publications



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