

Adding Preexisting Models to Xilinx Projects (ISE 7.1i)

Introduction:

In this tutorial, we shall discuss adding a preexisting model to a Xilinx ISE 7.1i project. For this example, we will be discussing adding the S3LEDencoder model to a schematic capture project. The first assumption is that the model already exists and we want to use it in our project. The next assumption is that the schematic symbol already exists. For this example, model is in the 'ECE Library' directory. Download the latest copy from the course website (typically in the form of a .zip file). It is a good practice to use the same model throughout the design process; if the designer desires updating the model care must be taken to ensure the new model is accepted in the on-going project and the ports match the previous model on the schematic symbol.

Steps:

1. Adding Model during new project setup (New Project Wizard)

- a. On **Add Existing Sources** pane, click **Add Source** button,
- b. Browse to the current *'ECE Library'* directory,
- c. Select desired Model, in this case choose 'S3LEDencoder.vhd', then click **Open** button,
- d. Select **VHDL Design File**, in the **Choose Source Type** dialog box, and click **OK** button,
- e. Ensure the **Copy to Project** checkbox is selected,
- f. If another model is desired repeat steps 1 thru 5 for the next model; else goto step 7,
- g. Click next and proceed as normal.

2. Adding Model during On-going Project Entry

- a. Right-click on any source in the Sources pane of Project Navigator and select **Add Copy of Source** menu item (there are other ways to select Add Copy of Source to the project, they will not be enumerated here),
 - b. Perform steps 1b thru 1d above.
- These steps can be used for adding Schematic Sheets to projects as well. Just select the .sch file in step 1c above, instead of the .vhd file.