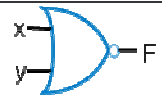


Quiz 6 – ECGR2181 – Fall 2008

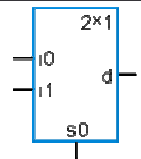
Your Name: _____

1. The ASCII string “49ER” is represented by the values 0x52576982
TRUE FALSE (1 point)
2. VHDL, Verilog, and Xilinx are all examples of hardware description languages used to synthesize digital circuits?
TRUE FALSE (1 point)
3. Any Boolean function can be implemented using just NAND gates.
TRUE FALSE (1 point)
4. The operation of adding the 8-bit 2’s complement number 0x7F to the 8-bit 2’s complement number 0xD0 and storing the result in a the 8-bit 2’s complement number will result in overflow.
TRUE FALSE (1 point)
5. The sign extension of the 8-bit 2’s complement number 0x80 to a 16-bit 2’s complement number is 0x8000.
TRUE FALSE (1 point)
6. What type of circuit is characterized by only active components with no memory? (1 point)
 - a. Sequential
 - b. Combinational
 - c. Holistic
 - d. Schematic
 - e. None of the above

7. What is the name of the device at the right? (1 point)
 - a. NOR
 - b. NAND
 - c. XOR
 - d. Decoder
 - e. None of the above



8. What is the name of the device at the right? (1 point)
 - a. XOR
 - b. Encoder
 - c. MUX
 - d. Decoder
 - e. None of the above



9. What is the name of the device at the right? (1 point)
 - a. NOR
 - b. Encoder
 - c. XOR
 - d. Decoder
 - e. None of the above



10. What is the name of the device at the right? (1 point)
 - a. XOR
 - b. Encoder
 - c. MUX
 - d. Decoder
 - e. None of the above

