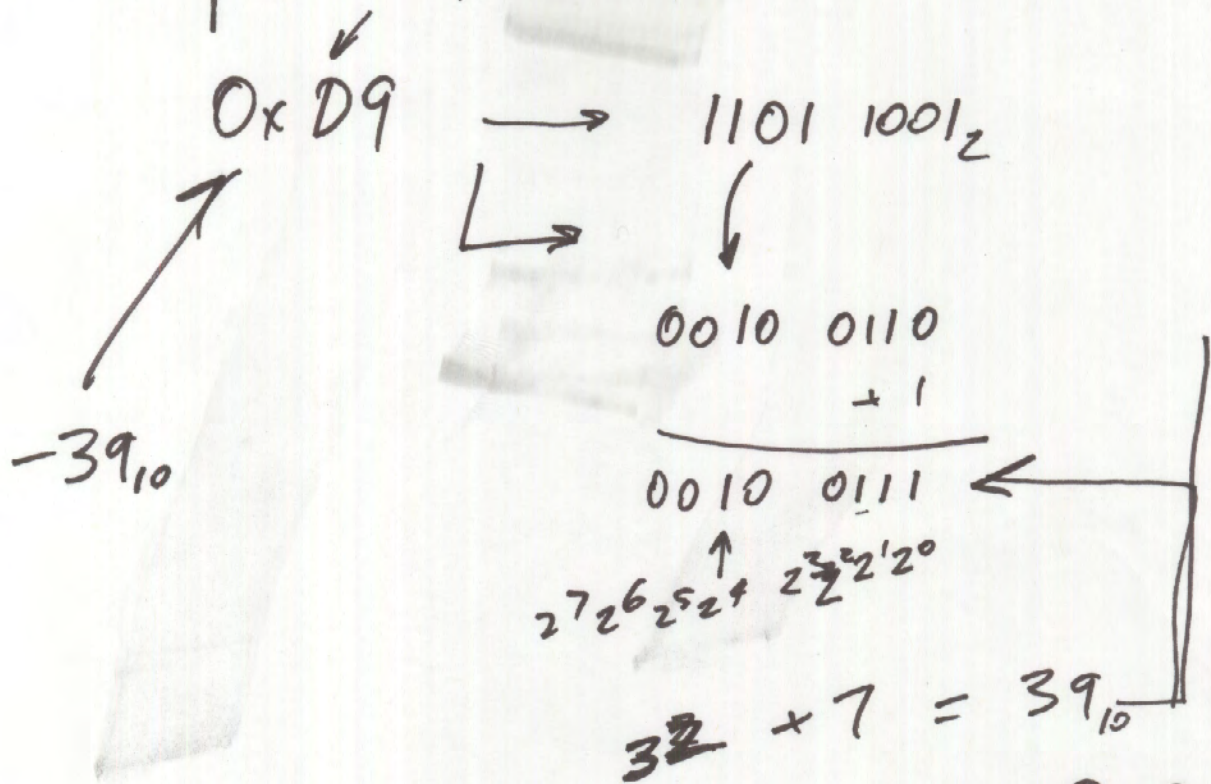
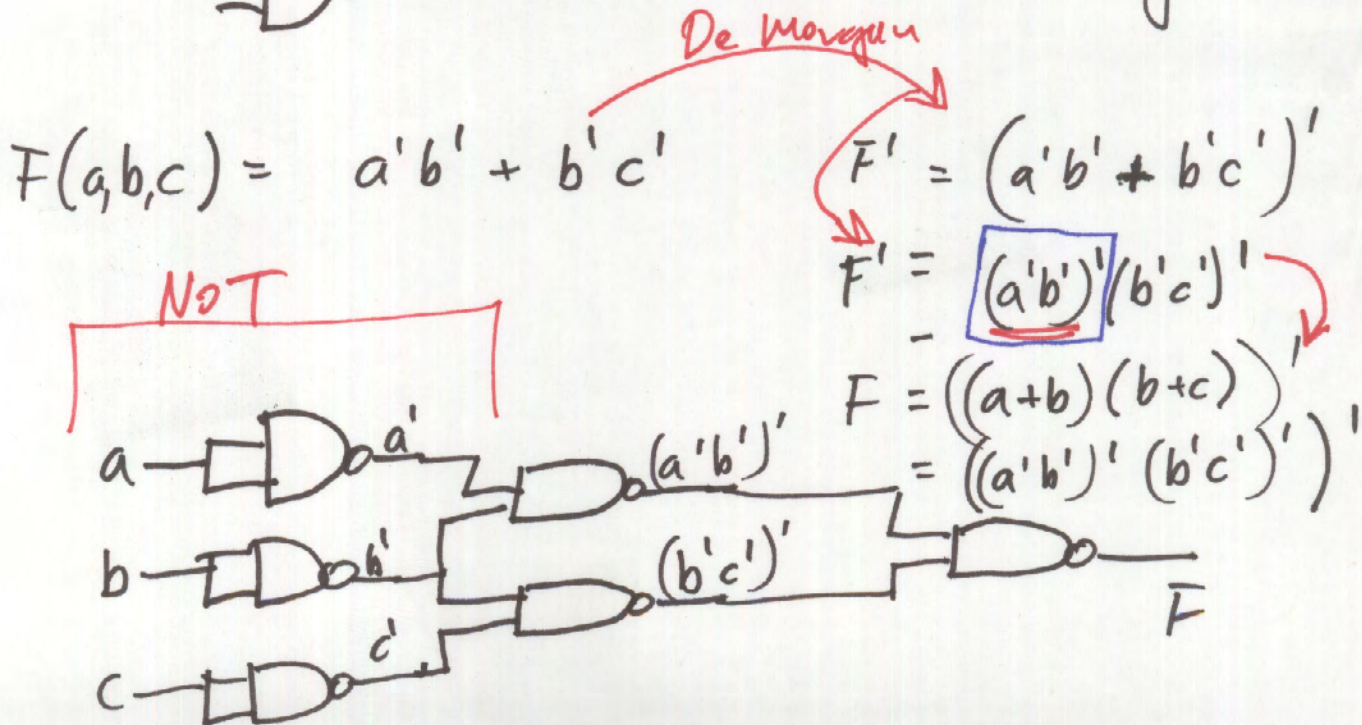


ECGR 2181 - Test Review 10/7/09
 2's complement, 8 bit



QED

Do This is a NAND gate...



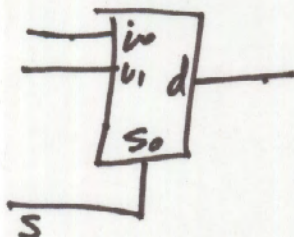
ECGR2181 - Test Renew

10/7/09 (2)

Make an 8 to 1 Mux using only

2 to 1 Muxes

s_0	d
0	i_0
1	i_1



$y_2 y_1 y_0$	Z
000	X_0
001	X_1
010	X_2
011	X_3
100	X_4
101	X_5
110	X_6
111	X_7

$8 = \text{data}, \quad 2^n = 8, \quad n = 3$

