ECGR 2181 CAD Demonstration Sheet

This sheet should be modified by the student to reflect the current lab assignment being demonstrated

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| --- | --- |
| Lab Number: | CAD 3 – |
| Team Members |

|  |
| --- |
| Team Member 1: |
| Team Member 2: |

 |
| Date: |  |

# Lab Demonstration Requirements

|  |  |  |  |
| --- | --- | --- | --- |
| REQ Number | Objective | Self-Review | TAReview |
| 1 | The switches are tied to the corresponding seven segment displays.  |  |  |
| 2 | The seven segment display reads the correct value for the switches |  |  |
| 3 | The switches are tied to the LEDs. |  |  |

# Code Requirements (will not be graded during lab demo)

|  |  |  |  |
| --- | --- | --- | --- |
| REQ Number | Objective | Self-Review | TAReview |
| 1 | Using structural VHDL, the encode entity is instantiated 4 times  |  |  |
| 2 | top.vhd is the top entity  |  |  |