

# ECGR #101 Quiz 3

9/8/08

- 1) Name (1 pt)
- 2) How wide is a Renesas Register (i.e. RO) # of bits 16 bits 2pts
- 3) How wide is the PC of the Renesas uControllers # of bits 20 bits 2pts
- 4) Which is not a real Assembly Language instruction:

JMP  
HOP  
PUSH  
POP

2pts

- 5) What is in a typical Flag register? 3pts

Any description of major flags (zero, negative, overflow, sign, carry)