Quiz 6 – ECGR 4101/5101

1) Name (1 point)

2) The operation of adding the 8-bit 2's complement number 0x7F to the 8-bit 2's complement number 0xD0 and storing the result in a the 8-bit 2's complement number will result in overflow.
   TRUE   **FALSE**   (2 points)

3) The sign extension of the 8-bit 2's complement number 0x80 to a 16-bit 2's complement number is 0x8000.
   TRUE   **FALSE**   (2 points)

4) The instruction exitd is used in Interrupt Functions:
   TRUE   **FALSE**   (2 points)

5) The stack pointer address is saved on the stack when a subroutine is called and the enter instruction is executed.
   TRUE   **FALSE**   (2 points)

6) What date is the first exam? **10/8/08** (1 point)