

Embedded Systems

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(1)

If UART sends a byte at
115,000 baudpps how often do you
need to load a byte into the
Transmission data register?
1 start, odd parity, 1 stop bit

95 μ sec

If SPI sends a longword at
115,000 bps, how often do you
need to load a long word into the
Transmission data register?

odd parity

$$\frac{33 \text{ bits}}{115,000} = 278 \text{ } \mu\text{sec}$$