

UNC Charlotte-ECGR4101/5101-Midterm Exam -10/14/09

Multiple Choice - Questions 1-10: Each of these multiple choice questions is worth 4 points for a correct answer, 0 points for an incorrect answer. Circle your answers on the bubble sheet.

		int i, sum;		
1)	and the second s	sum = 0;		
	a. Add all the numbers from 0 to 100 and put the result in sum.	i = 0;		
	b. Add all the odd numbers from 0 to 100 and put the result in sum.	for(i=N;i<100;i++){		
	c. Add all the even numbers from 0 to 100 and put the result in sum.	if((i % 2))		
	d. Add all the odd numbers from N to 100 and put the result in sum.	sum += i;		
	e. Add all the even numbers from N to 100 and put the result in sum.	, }		
2)				
	mode what areas in memory could be addressed.			
	(a.) Only internal areas (SFR, internal RAM, internal ROM)			
	b. Internal areas (SFR, internal RAM, internal ROM) and external memory an	reas		
	c. SFR, internal RAM and external memory			
	d. SFR, external memory			
	e. Only external memory			
3)	What is the number of address bits required to address any byte in a memory the	nat contains 65536 bytes?		
		. 20		

4) If I used the MCU30262 instruction 'enter #2' at the beginning of a function, how many bytes does the corresponding 'exitd' instruction deallocate from the stack if no other enter, exitd, jsr, pop or push operations are performed within the function? e. 10 b. 7 c. 8

```
5) Which assembly language code below adds the value of global variable shoe to the value in R1 and stores the
   result in R1:
                                                             d. add.w _shoe, R1
   a. add.w shoe, R1
                                                             e. add shoe + R1
```

b. add.w R1, shoe

c. add shoe, R1

6) How far apart in time can two interrupts be and still be considered as simultaneous (assume a 16 MHz clock)?

92 ns d. 40 ps 62.5 ns e. None of the above c. 50 ns

Consider the piece of C-code to the right for questions 7 and 8:

7) How is the char b in the code passed to the function choosechar(a,b)? d. R1H Stack

b. R1L e. R2 c. R1

f. None of the above

8) How is the int d passed in the function dothernath(c,d)?

d. R1H Stack e R2 b. R1L f. None of the above c. R1

```
char a = 'x';
char b = 'a';
char new;
int d = 5;
float c = 3.14;
float result;
new = choosechar(a,b);
result = dothemath(c,d);
```

main(){

9) The double word 0x AC35 FA34 is stored in memory addresses N through N+3. Which of the following represents storage using big endianness?

a.	N	43
	N+1	AF
	N+2	53
	N+3	CA

(c.)	N	AC
U	N+1	35
	N+2	FA
	N+3	34

N	34
N+1	FA
N+2	35
N+3	AC

b.	N	FA
	N+1	34
	N+2	AC
	N+3	35

d.	N	FA
	N+1	34
	N+2	AC
	N+3	35

- 10) According the ECGR4101/5101 programming standards, which of the following should be included in the header of a subroutine?
 - a. Subroutine name
 - b. Assumptions
 - c. Inputs/Outputs

d.	Interfaces
(e.)	All of the above

Short Answer

- \$11) You have several 1Ahr 1.2V batteries and a LED that has an average drain of 3.5mA at 4.8V.
 - a. Draw the configuration of the minimum number of batteries needed to light the LED.(2 points)
 - b. With your battery configuration how long would the LED stay lit? (3 points)
- 12) Given the following information of a particular analog to digital converter, determine the value of the digitally represented voltage and the step size of the converter. (10 points)
 - The device is a 10-bit ADC with a + reference voltage of 3.3 volts and a reference voltage of -0 volts.
 - The digital representation is: 0100110010.
- 13) What are the benefits of a microprocessor/microcontroller-based embedded system over an FPGA-based embedded system? (10 points) (in three to five sentences)
- \$ 14) Can you, the user, store a value at memory location 00CBCh? Why or why not? (5 points)
 - 15) Write the code to set up an A/D conversion for the first four channels of P2, 10-bit sample-and-hold, one shot and store the data in four variables (unsigned *int sample0, sample1, sample2, sample3*). Include all set-up bytes needed. If you use any .h files, tell me which ones you use. (25 points)
 - 16) A) Imagine you have an embedded system that uses your QSK board. The system will:
 - Req. 1: Use the C programming language.
 - Req. 2: Continually poll SW1. While it is pressed, light the LED1.
 - Req. 3: Continually poll SW2. While it is pressed, light the LED2.
 - Req. 4: Continually poll SW3. While it is pressed, light the LED3.
 - Req. 5: Two or three LEDs can be lit at the same time.
 - Req. 6: Continually have an ISR put an analog value on Port 0, bit 0 put it in the variable *printme*.
 - Reg. 7: Continually print the value in *printme* on the LCD.
 - Req. 8: You may not use any string libraries.
 - Req. 9: Include a few comments, including headers.

Identify the "architecture" of this problem, dividing he work into appropriate functions. Substantiate your design decisions. (20 points)

B) Write the algorithms (general steps) which implements the above functionality of all functions. (35 points)

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11	a) Need to get up to 4.8v from 1.2v cells
	The Zpts
	No need torx "dahle bank" - I did not ask for
11	b) 1 Ahr/0.0035 A = 285.7 hr (11.9 days)
12)	b) $1 \text{Ahr} / 0.0035 \text{A} = 285.7 \text{ hr} (11.9 \text{ days})$ 3 pts all or 4 hothing $4 \text{step } 5 \text{i} 2e = 3.3 \text{v} / 2^{10} = 3.22 \text{mv}$ 5points $4 \text{pumber } 0.100110010 = 306_{10}$ 2for consept $4 \text{pumber } 0.20110010 = 306_{10}$ 2for consept
	Mumber 0 / 00 11 00 10 = 306,0 2 for consept answer
	306 + 3.27 mv = 0.986 v ± 0.005 v Spoints 3 for concept Z for correct answer
13)	Notes, P1-11
	Pros of Microcontroller-based system: 11st two paints. Unit cost, power consumption convertly everything else is about the same or better on an FRGA
14)	PAM Pange x0400 to x7FFF, but Spouts, x07F7F to x07FFF is reserved. all or nothing Therefore xCBC is well within the allowed range of RAM

ECGR 4101/5101- Exam I Solution - Fall 2009 15) To do Nis: Notes a "one shot" * set up control registers

* start single sweep

* read each reg for several pinsis a sweep --8 if you did four one shots of individual pins ADCONO = 0xBO; /* CHO = 0 -DX 30 Spts TRG=1 ADST = 0 Not yet! CKSO = any SCAND= 1 ANOJO AN3 ADCON1=0x 29; MDZ= 0 BMS= 1 10 bits 0x 39 CKS 1= any (0 or 1) 5/15 VCVT - 1 Connect OPA0= 0 OPAI-O ASAP: 1 = Sample/hold ADGSELO:1 = Port2 ADOSEL1=1 CKSZ = any (oor1) Spts 65 ADST = 1; / Now convert

10pb Sample 0 = ADO & 0x03AF; Sample 1 = AD 1 & 0x03AF; Sample Z = AD Z & 0x03AF;

sample 3 AD3& 0x03ff;

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	,		
Sotup SWULD	Set up ADC	ADE-ISR	Convert Thosa
Will run once Sots up direction of SW& LEDS	Set up ADC to read Convect port and continuasly read	Peads ADC regrator into global var	Couverts value binary theory to ASCII Value
	Ardritect	ure - Apt	s each
Main	Problem	16	

checks the SW and value Convorts House and displays

ECGR 4101/5101 - Midterm Solution - Fall 2009 NOTE - I NEVER asked for code for this problem 11 Setup_SW_LED 11 Sets up 5 was imputs, LEDS as output, turns LEDS Off Set Switches as inputs Set LEDS as outputs Turn off LEDS 11 Set up ADC to read bit 0 for post 0, continually read Configure ARCONO Configure ADCONI 5 pt3 Confidure ADCONZ 1/ ADC-15R 11 Read Did O & Save 5 pts printine = adO & OXO3FF 11 Convert the designation to an ASCII String Convert thousands Convert hundreds lopts Corvert tens convert ones deplay // wain 10 pts setup-SW-LED setup-ADC Starf ADC while (1) & if swipressed, light LEDI, else town off LEDI if swipressed, Light LEDI, else two off LEDI if Swipressed, Light LEDI, else two off LEDI Convert Value These mostallow all to Couvert Valve Tracht