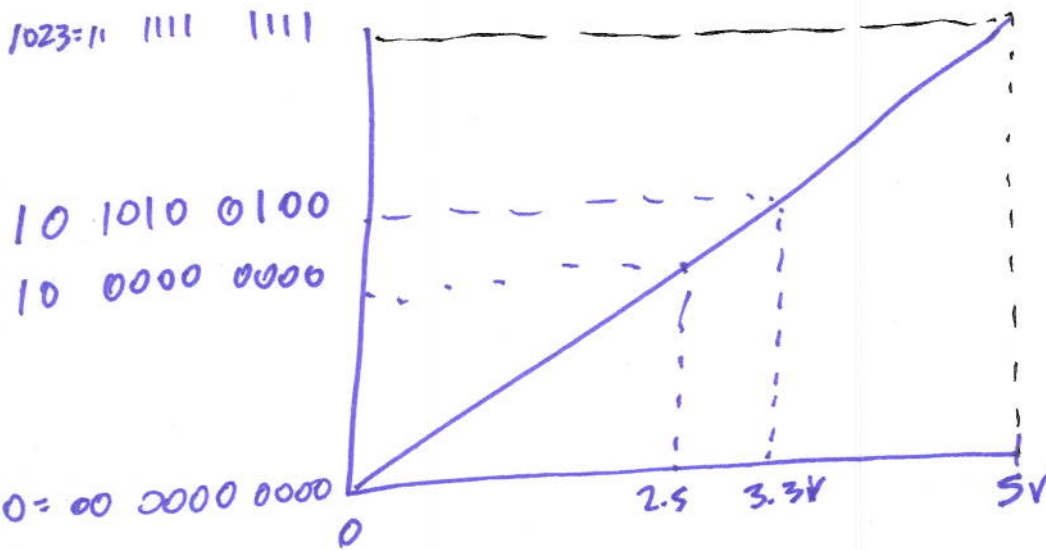


Lab 4 extra notes or fixes:

1. Step 5 same process as used in tutorial 3 we need to copy and paste in order for us to get the x axis and Y axis, *then compile before 2*
2. On step 6 instead of "Right click on *FPGA Target* in the project explorer window, and create a new VI"
 Right click on *Starter Kit sbRIO (IP address)* in the project explorer window, and create a new VI
 Otherwise you will not see all the functionality
3. Step 10 should be figures 4.8a and 4.8b and 4.8c
4. And the logic should be True False False
5. False true False
6. False False True

$$676_{10} = 2A4_{16} = 1010100100_2$$



Step Size: $\frac{0.0129V}{0.0129V} = \frac{3.3}{256}$

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Guess 1 50 ↓
 2 25 ↑
 3 37 ↑
 4 43 ↓
 5 40 ↑
 6 42 ↓
7 = 41!

②

Range 55° to 90° F

~~35~~° → 6 bits

$$2^6 = 64$$

35 to 10ths

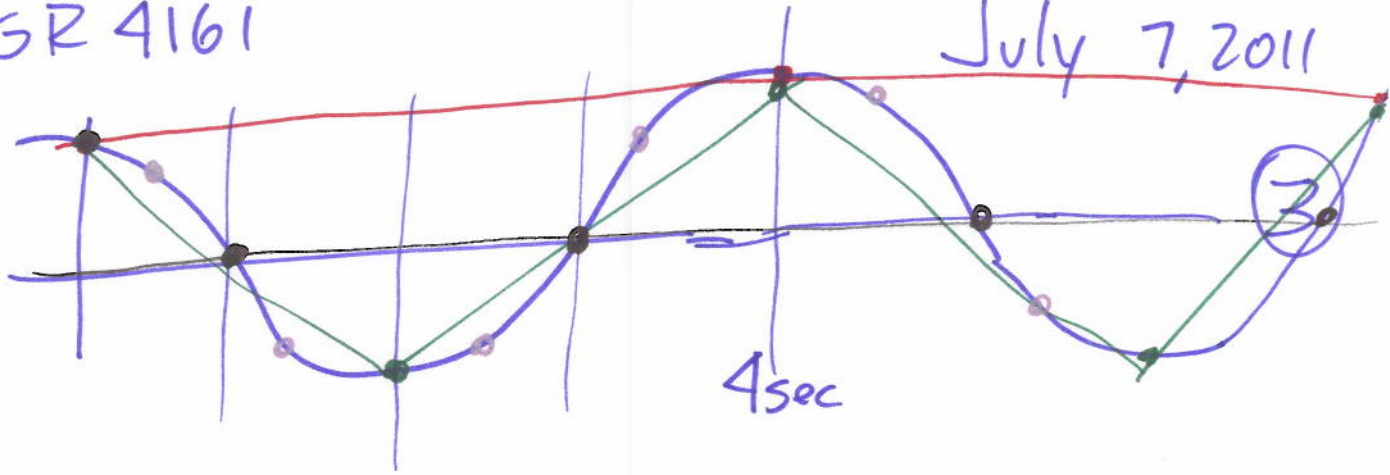
350 → a bits

$$2^9 = 512$$

The Number = 41

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$$f = \frac{1}{4} \text{ Hz}$$

measure $f = \frac{1}{4} \text{ Hz}$ •

measure $f = \frac{1}{2} \text{ Hz}$ •

measure $f = \frac{1}{2} \text{ Hz}$ •

measure $f = 1 \text{ Hz}$