

ECGR3/5/6090 Acronym List

Acronym	Expansion
*ALPM	Assembly language Programming manual
*DS	Data Sheet
*PM	Programming Manual
ADC	Analog to Digital converter
API	Application Programming Interface
ASIC	Application Specific Integrated Circuit
bss	block started by symbol
BTST	Bit Test
CMM	Capability Maturity Model
CRT	C Run Time
DAC	Digital to Analog converter
DMA	Direct Memory Access
DMAC	Direct Memory Access Controller
DRAM	Dynamic Random Access Memory
EEPROM	Electrically Erasable Programmable Read only M
FB	Frame Base
FCFS	First Come First Served
FLG	Flag
ICE	In Circuit Emulator
INTB	Interrupt Table Register
ISA	Instruction Set Architecture
ISO	International Standards Organization
ISP	Interrupt Stack Pointer
ISR	Interrupt Service Routine
JTAG	Joint Test Action Group
LCD	Liquid Crystal Display
LED	Light Emitting Diode
LLSC	Load Linked, Store Conditional
MCU	Micro-Controller Unit
NMI	Non Maskable Interrupt
OSI	Open Systems Interconnection
PC	Program Counter
PCB	Printed Circuit Board / Process Control Block
PWM	Pulse Width Modulator
RAM	Random Access Memory
RMS	Rate Monotonic Scheduling
RR	Round Robin
RTOS	Real-Time Operating System
SB	Static Base Register
SCM	Software Configuration Management
SJF	Shortest Job First
SP	Stack Pointer
SRAM	Static RAM
TCB	Thread Control Block
UART	Universal Asynchronous Reciever Transmitter
UB	Utilization Bound
USP	User Stack Pointer
WDT	WatchDog Timer
* - usually has prefix	M (micro-controller)